



4. Provide pseudocode for a radix sort algorithm and illustrate its operation on the following input.

39 10 3 89 24 26 58 74 26 48

5. Repeat Question 4 for merge sort.



10. Explain the reasoning behind treating each thread block as a processor for the purpose of algorithm analysis.

11. What is CRCW PRAM?

12. The paper does not list any code or pseudocode. How may we study the details of the implementations described in the paper, perhaps to improve on them?

13. What is stable sorting and why is it important in radix sorting?

14. Describe the salient details of Figure 1 from the paper as precisely and concisely as possible. That is, convey the important points without using the figure. Repeat for Figure 2.

15. How does the paper's implementation of GPU radix sort take advantage of the GPU *shared memory*?

16. Describe *sorting by 1-bit splits* and illustrate its action on the data of Question 4.

[additional space for answering the earlier question]